

a temperature controller positioned and operable to maintain the at least one wafer at a temperature equal to or lower than about a dew point of the solvent;

a gas source of at least one reactive gas coupled to the chamber so as to deliver such gas to the chamber, the at least one reactive gas being selected to chemically react with the surface of the wafer to clean the wafer; and wherein

C1  
COND.  
the liquid solvent comprises a transport medium which carries at least some of the at least one reactive gas through the film to said at least one of the first and second wafer side surfaces where the at least one reactive gas chemically reacts with said at least one of the first and second wafer side surfaces.

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42. (Three Times Amended) An apparatus for delivering ozone gas to the surface of a wafer comprising:

a wafer receiving chamber;

a wafer carrier positioned within the chamber;

C2  
at least one wafer positioned in the wafer carrier in a substantially vertical position within the wafer receiving chamber;

a liquid depositor adapted to produce a stream of liquid solvent and form a layer of the liquid solvent on at least one major surface of a wafer supported by the wafer carrier within the chamber, wherein the stream is produced in a direction substantially parallel to the at least one major surface of the wafer, the layer of liquid solvent being inert to said at least one major surface of the wafer;

an ozone gas source coupled to the chamber so as to deliver ozone gas to the chamber and increase the concentration of ozone gas within the chamber;

C2  
CMB  
the liquid solvent layer transporting ozone gas to the surface of the wafer to thereby expose the wafer surface to ozone.

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44. (Twice Amended) An apparatus for cleaning semi-conductor wafers comprising:

a chamber sized to receive at least one wafer to be cleaned;

C3  
a reactant gas source inlet and outlet, the inlet and outlet each communicating with the chamber and defining a gas flow path for reactant gas from the inlet to the outlet;

a reactant gas source coupled to the inlet such that reactant gas is delivered from the inlet and flows in the gas flow path to the outlet;

a wafer carrier positioned within the chamber and supporting at least one wafer at least partially in the gas flow path;

a liquid layer former coupled to the chamber and operable to form a layer of liquid on at least one major surface of a wafer supported within the chamber, the liquid being selected so as to be substantially non-chemically-reactive with the reactant gas and the wafer, whereby the reactant gas is transported through the liquid layer to the wafer surface, the reactant gas being selected so as to chemically react with components on the surface of the wafer to clean the wafer; and

CS2  
amended

a temperature controller configured and operable to cool the at least one wafer in the chamber such that the liquid layer on the at least one major surface of the wafer is formed by condensation.

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46. (Twice Amended) An apparatus for stripping photo-resist from semiconductor wafers comprising:

a film former adapted to condense a solvent to form a film of liquid solvent onto a surface of the wafer which is to be stripped of photo-resist, the film of liquid solvent being substantially non-chemically reactive with the photo-resist;

CA

a gas exposer adapted to expose the film of liquid solvent to a source of at least one reactant gas which is substantially non-chemically reactive with the solvent and which is chemically reactive with the photo-resist so as to strip the photo-resist from the wafer surface;

a cooling mechanism operable to cool the surface of the wafer; and

whereby reactant gas is transported through the film of liquid solvent to the wafer surface.

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50. (Once Amended) An apparatus for cleaning semi-conductor wafers, the wafers having first and second wafer side surfaces, the apparatus comprising:

CS

a chamber sized to receive at least one wafer to be cleaned;

a solvent applicator coupled to the chamber and adapted to provide a vaporized solvent to at least one of the first and second side surfaces of the wafer positioned within the

chamber so as to condense the vaporized solvent on the at least one of the first and second wafer side surfaces to form a thin layer of solvent thereon, the solvent being selected to be substantially non-chemically reactive with the wafer;

a gas source of at least one reactive gas coupled to the chamber so as to deliver such gas to the chamber, the at least one reactive gas being selected to chemically react with the surface of the wafer to clean the wafer; and wherein

the solvent layer dissolves at least some of the at least one reactive gas in the film such that dissolved gas is brought into direct contact with and chemically reacts with the at least one of the first and second wafer side surfaces.

C5  
am-12.

51. (Once Amended) An apparatus for cleaning semi-conductor wafers, the wafers having first and second wafer side surfaces, the apparatus comprising:

a chamber sized to receive at least one wafer to be cleaned;

a solvent applicator coupled to the chamber and adapted to vaporize a solvent and condense the solvent on at least one of the first and second side surfaces of the wafer positioned within the chamber so as to form a film of condensed liquid solvent on the at least one of the first and second wafer side surfaces, the solvent being substantially inert to the wafer;

a gas source of at least one reactive gas coupled to the chamber so as to deliver such gas to the chamber, the at least one reactive gas being selected to chemically react with the surface of the wafer to clean the wafer; and wherein

the condensed liquid solvent comprises a transport medium which dissolves at least some of the at least one reactive gas in the film to the at least one of the first and second

CS-  
entp. wafer side surfaces where the at least one reactive gas chemically reacts with the at least one of  
the first and second wafer side surfaces.

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55. (Once Amended) An apparatus for cleaning semi-conductor wafers, the wafers having first and second wafer side surfaces, the apparatus comprising:

a chamber sized to receive at least one wafer to be cleaned;

CF a solvent applicator coupled to the chamber and adapted to drip solvent onto at least one of the first and second wafer side surfaces so as to form a film of liquid solvent on the at least one of the first and second wafer side surfaces, the solvent being substantially non-chemically reactive with the first or second wafer side surfaces;

a temperature control device adapted to cool the at least one wafer;

a gas source of at least one reactive gas coupled to the chamber so as to deliver such gas to the chamber, the at least one reactive gas being selected to chemically react with the surface of the wafer to clean the wafer; and wherein

the liquid solvent comprises a transport medium which dissolves at least some of the at least one reactive gas in the film where the dissolved gas is brought into direct contact with and chemically reacts with the at least one of the first and second wafer side surfaces.

56. (Once Amended) An apparatus for cleaning semi-conductor wafers, the wafers having first and second wafer side surfaces, the apparatus comprising:

a chamber sized to receive at least one wafer to be cleaned;

a nebulizer adapted to create a mist of a solvent;

a temperature control device operable to cool the wafer such that the mist of solvent condenses on at least one of the first and second wafer side surfaces so as to form a film of liquid solvent on the at least one of the first and second wafer side surfaces, the solvent being substantially non-chemically reactive with a side surface of the wafer;

alg comp.  
a gas source of at least one reactive gas coupled to the chamber so as to deliver such gas to the chamber, the at least one reactive gas being selected to chemically react with the surface of the wafer to clean the wafer; and wherein

the liquid solvent comprises a transport medium that dissolves at least some of the at least one reactive gas in the film where the dissolved gas is brought into direct contact with and chemically reacts with the at least one of the first and second wafer side surfaces.

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### REMARKS

Claims 39-56 are pending in the present application. Reconsideration is requested.

Claims 39-41, 43-48, and 50-55 are rejected under 35 U.S.C. § 102(e) as allegedly being anticipated by U.S. Patent No. 5,762,755 to McNeilly et al., (hereinafter McNeilly). Claims 42, 49, and 56 are rejected under 103(a) as allegedly being unpatentable over McNeilly in view of U.S. Patent No. 4,795,497 to McConnell et al., and U.S. Patent No. 4,946,549 to Bachman et al. Applicants continue to traverse each of these objections.

Briefly, McNeilly discloses an apparatus 1 in which ozone is introduced into a chamber 2 for pre-cleaning contaminants from a wafer. Following the pre-cleaning step (i.e., after the ozone has reacted with the wafer), the wafer is etched using conventional etching techniques (either vapor phase HF/H<sub>2</sub>O oxide or Cl<sub>2</sub>/UV silicon etching). See col. 2, lines 29-32 and FIG. 1.